

Please amend the claims as follows:

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1. (further amended) A translation loop modulator for a transmission circuit in a communication system having a first mode of operation at a first frequency and a second mode of operation at a second frequency, said translation loop modulator comprising:

input modulation means for receiving at least one input signal that is representative of information to be modulated, for receiving a feedback signal, and for producing an intermediate modulated signal responsive to said input signal and said feedback signal;

comparator means for receiving said intermediate modulated signal and a reference signal having a frequency of  $F_{LO}$ , and for producing an output transmission signal having a frequency  $F_{OUT}$  responsive to said intermediate modulated signal, wherein said comparator means includes a first frequency divider unit for providing a divide by  $m$  function and a second frequency divider unit for providing a divide by  $n$  function such that  $[F_{LO} = F_{OUT} / (1 \pm m/n)]$   
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 $F_{LO} = F_{OUT} / (1 + m/n)$  in said first mode of operation and  $F_{LO} = F_{OUT} / (1 - m/n)$  in said second mode of operation and

feedback circuitry coupled to said output transmission signal, coupled to said reference signal and coupled to said input modulation means, said feedback circuitry for producing said feedback signal responsive to said output transmission signal and said reference signal.

2. (original) A translation loop modulator as claimed in claim 1 further comprising a reference loop modulator for producing said reference signal.

3. (original) A translation loop modulator as claimed in claim 2, wherein said reference

2 loop modulator includes a fractional  $n$  synthesizer.

1 4. (original) A translation loop modulator as claimed in claim 1, wherein said comparator  
2 means includes at least one frequency divider unit including an input port for receiving a first  
3 signal having a first frequency, and an output port for producing a second signal responsive to  
4 said first signal, said second signal having a second frequency of a predetermined relationship  
5 to the frequency of said first signal.

1 5. (previously amended) A translation loop modulator as claimed in claim 1, wherein an  
2 input port of said second frequency divider unit is coupled to said reference signal, and an  
3 output port of said second frequency divider unit is coupled to a phase comparator device.

1 6. (original) A translation loop modulator as claimed in claim 4, wherein said comparator  
2 means includes a second frequency divider unit including an input port for receiving a first  
3 signal having a first frequency, and an output port for producing a second signal responsive to  
4 said first signal, said second signal having a second frequency of a predetermined relationship  
5 to the frequency of said first signal.

1 7. (previously amended) A translation loop modulator as claimed in claim 1, wherein an  
2 input port of said first frequency divider unit is coupled to said intermediate modulated signal,  
3 and an output port of said first frequency divider unit is coupled to a phase comparator device.

1 8. (original) A translation loop modulator as claimed in claim 1, wherein said feedback

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2 circuitry includes a mixer device including a first input port coupled to said output transmission  
3 signal, a second input port coupled to said reference signal, and an output port coupled to said  
4 feedback signal.

1 9. (original) A translation loop modulator as claimed in claim 8, wherein said reference  
2 signal is directly connected to said mixer device.

1 10. (further amended) A translation loop modulator for a transmission circuit in a  
2 communication system having a first mode of operation at a first frequency and a second mode  
3 of operation at a second frequency, said translation loop modulator comprising:

4 quadrature modulation means for receiving at least one input signal that is  
5 representative of information to be modulated, for receiving a feedback signal, and for  
6 producing an quadrature modulated signal responsive to said input signal and said feedback  
7 signal;

8 phase comparator means for receiving said quadrature modulated signal and a reference  
9 signal having a frequency  $F_{LO}$ , and for producing a phase comparator signal responsive to said  
10 quadrature modulated signal and said reference signal, said phase comparator means including  
11 a first frequency divider unit for providing a divide by  $m$  function and a second frequency  
12 divider unit for providing a divide by  $n$  function;

13 oscillator means for receiving said phase comparator signal, and for producing an  
14 output transmission signal responsive to said phase comparator signal, said output transmission  
15 signal having a frequency  $F_{OUT}$  wherein  ~~$[F_{LO} = F_{OUT} / (1 \pm m/n)]$~~   $F_{LO} = F_{OUT} / (1 + m/n)$  in

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16 said first mode of operation and  $F_{LO} = F_{OUT} / (1 - m/n)$  in said second mode of operation; and  
17 feedback circuitry coupled to said output transmission signal, coupled to said reference  
18 signal and coupled to said quadrature modulation means, said feedback circuitry for producing  
19 said feedback signal responsive to said output transmission signal and said reference signal.

1 11. (original) A translation loop modulator as claimed in claim 10 further comprising a  
2 reference loop modulator for producing said reference signal.

1 12. (original) A translation loop modulator as claimed in claim 11, wherein said reference  
2 loop modulator includes a fractional  $n$  synthesizer.

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1 13. (original) A translation loop modulator as claimed in claim 10, wherein said comparator  
2 means includes at least one frequency divider unit including an input port for receiving a first  
3 signal having a first frequency, and an output port for producing a second signal responsive to  
4 said first signal, said second signal having a second frequency of a predetermined relationship  
5 to the frequency of said first signal.

1 14. (previously amended) A translation loop modulator as claimed in claim 10, wherein an  
2 input port of said second frequency divider unit is coupled to said reference signal, and an  
3 output port of said second frequency divider unit is coupled to a phase comparator device.

1 15. (original) A translation loop modulator as claimed in claim 13, wherein said comparator  
2 means includes a second frequency divider unit including an input port for receiving a first

3 signal having a first frequency, and an output port for producing a second signal responsive to  
4 said first signal, said second signal having a second frequency of a predetermined relationship  
5 to the frequency of said first signal.

1 16. (previously amended) A translation loop modulator as claimed in claim 10, wherein an  
2 input port of said first frequency divider unit is coupled to said intermediate modulated signal,  
3 and an output port of said first frequency divider unit is coupled to a phase comparator device.

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cont 1 17. (original) A translation loop modulator as claimed in claim 10, wherein said feedback  
2 circuitry includes a mixer device including a first input port coupled to said output transmission  
3 signal, a second input port coupled to said reference signal, and an output port coupled to said  
4 feedback signal.

1 18. (previsously amended) A translation loop modulator as claimed in claim 17, wherein  
2 said reference signal is directly connected to said mixer device.

1 19. (further amended) A translation loop modulator for a transmission circuit in a  
2 communication system having a first mode of operation at a first frequency and a second mode  
3 of operation at a second frequency, said translation loop modulator comprising:

4 quadrature modulation means for receiving at least one input signal that is  
5 representative of information to be modulated, for receiving a feedback signal, and for  
6 producing an quadrature modulated signal responsive to said input signal and said feedback

7 signal;

8 first frequency divider means for receiving said quadrature modulated signal, and for  
9 producing a first frequency divided signal responsive to said quadrature modulated signal such  
10 that said first frequency divider means provides a divide by  $m$  function;

11 second frequency divider means for receiving a reference signal having a frequency  
12  $F_{LO}$ , and for producing a second frequency divided signal responsive to said reference signal  
13 such that said second frequency divider means provides a divide by  $n$  function;

14 phase comparator means for receiving said first frequency divided signal and said  
15 second frequency divided signal, and for producing a phase comparator signal responsive to  
16 said first and second frequency divided signals;

17 oscillator means for receiving said phase comparator signal, and for producing an  
18 output transmission signal having a frequency  $F_{OUT}$  responsive to said phase comparator signal  
19 such that  $[F_{LO} = F_{OUT} / (1 \pm m/n)]$   $F_{LO} = F_{OUT} / (1 + m/n)$  in said first mode of operation and  
20  $F_{LO} = F_{OUT} / (1 - m/n)$  in said second mode of operation; and

21 feedback circuitry coupled to said output transmission signal, coupled to said reference  
22 signal and coupled to said quadrature modulation means, said feedback circuitry for producing  
23 said feedback signal responsive to said output transmission signal and said reference signal.

1 20. (original) A translation loop modulator as claimed in claim 19 further comprising a  
2 reference loop modulator for producing said reference signal.

1 21. (new) A translation loop modulator as claimed in claim 19, wherein said first mode of

2 operation is at about 1800 MHz.

1 22. (new) A translation loop modulator as claimed in claim 19, wherein said second mode

2 of operation is at about 900 MHz.